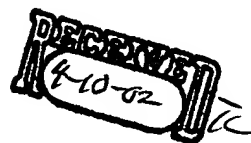


Response
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IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Applicant(s): **Brij Bhushan Garg, Donald James Wemple**
Case: **Garg 2-3**
Serial No.: **09/191,708** Filed: **November 13, 1998**
Examiner: **Joseph B. Logsdon** Group Art Unit: **2662**
Title: **SPACE/TIME SWITCH ARCHITECTURE**

RESPONSE

ASSISTANT COMMISSIONER FOR PATENTS
Box AF
WASHINGTON, D.C. 20231

SIR:

This is in response to the Final Office Action mailed February 25, 2002.
Please amend the above, identified patent application as follows:

IN THE CLAIMS

Please amend claims 1, 5, 6, 11, 18, and 21 as follows:

1. (Amended) Apparatus for switching data from any of a plurality of inputs to any of a plurality of outputs, comprising:
apparatus for receiving a plurality of input bit packs organized in a combination of input data rails and time slots;

Do not enter
JMB